

DCPS Workshop / Tutorial Announcement

“Error Resilience in Nano-Electronic Circuits and Systems”

We announce a tutorial and workshop within the frame of the DCPS Project, supported by the German Academic Exchange Service (DAAD). The event will take place on Friday, May 29th, 2015, at BTU Cottbus-Senftenberg (main Campus), Location: Verfügungsgebäude 1 C (Informatik- Building), Room 0.01.

Preliminary Schedule:

9:00 Opening and Welcome

9:10 - 10:30 Tutorial by Prof. Dr. Krishnendu Chakrabarty, Duke University:
“Resilient System Design Using Machine Learning and Error Prediction”

10:30 - 10:45 Coffee Break

10:45- 12:00 Tutorial by Prof. Dr. Andreas Steininger, Vienna University of Technology:
“Mitigating transient faults in GALS Architectures”

12:00 – 13:00 Lunch Break

13:00-13:30 Short Tutorial on “Fault Tolerant Pipeline-Based Architectures” by Stefan Weidling, Univ. of Potsdam and IHP

13:30-14:00 Short Tutorial on “Combined Detection and Correction of Delay Faults and Transient Faults” by H. T. Vierhaus, BTU-CS

14:00-14:30 Short tutorial on “ Latch-Up Protection and Radhard Design Flow”
by Wladimir Petrovic of IHP

14:30-15:30 Coffee and Discussions with Industrial Guests (Industry Panel)

15:30 End

For external participants / visitors:

Guests from partner universities and associated partners are, as always, welcome. Please, register with Kathleen Galke at BTU-CS: kl@informatik.tu-cottbus.de

Please, note: Travel can be supported by funds from DAAD for students from Tallinn, Liberec, Poznan and Gliwice with 200 € (travel only) and 50 € per day. For cash payments, students have to register at latest 10 days before the event!